

# R&D on a novel polarimeter spectro-imager with Micromegas detectors and a Caliste readout system

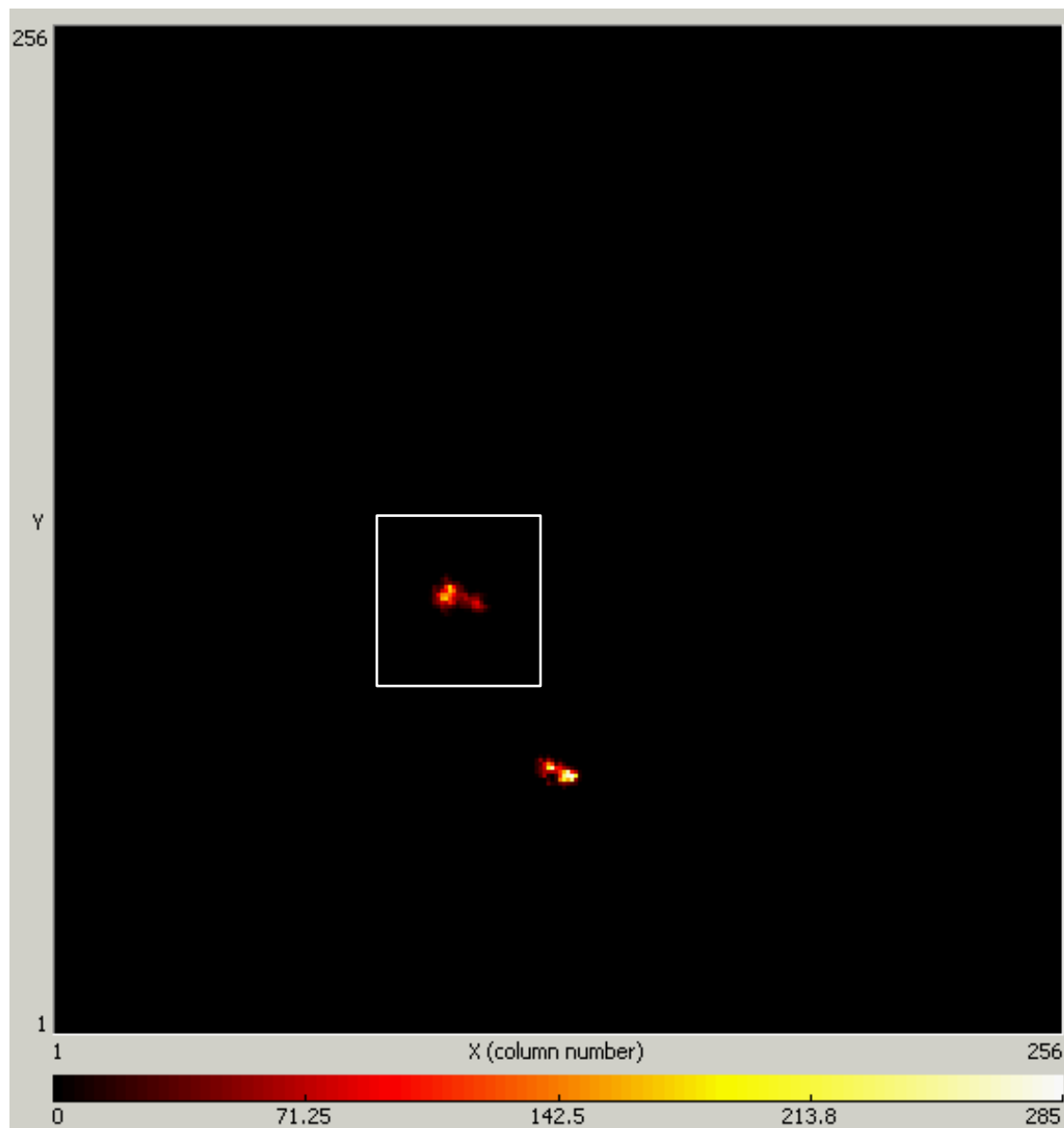
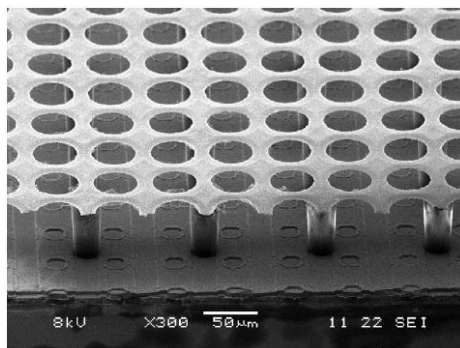
D. Attié, C. Blondel, L. Boilevin-Kayl, D. Desforges, E. Ferrer-Ribas, I. Gomataris,  
O. Gevin, F. Jeanneau, O. Limousin, A. Meuris, T. Papaevangelou, A. Peyaud

CEA Saclay/Irfu

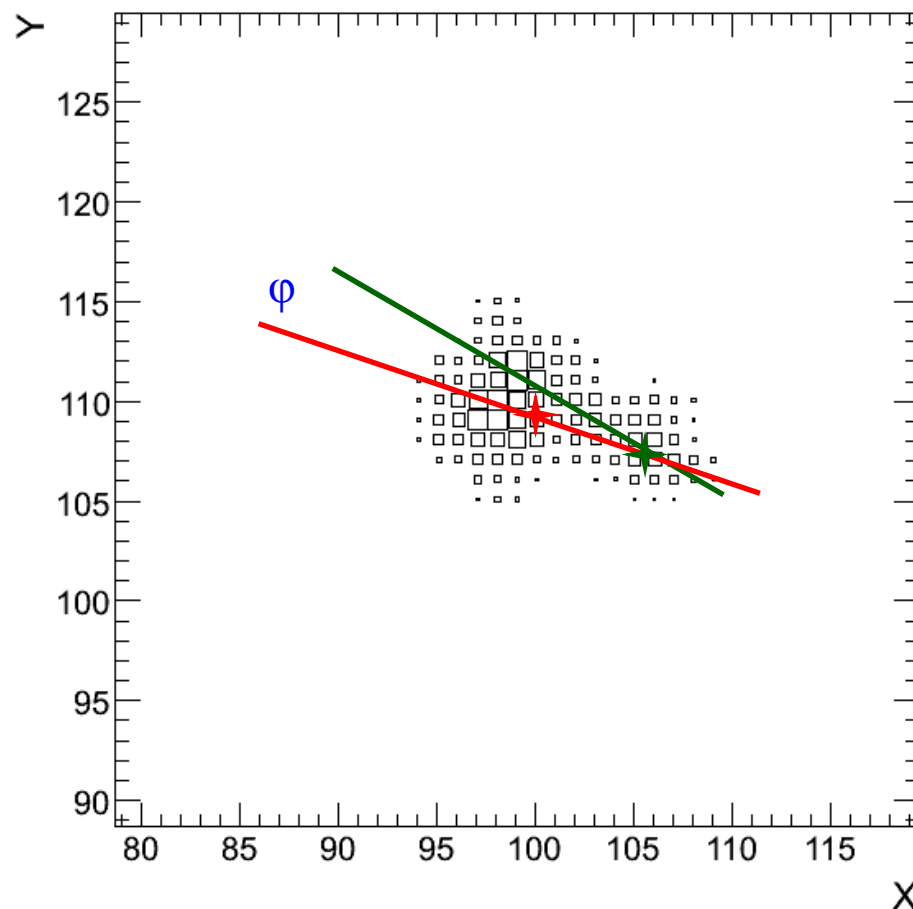


- Polarimetry capability using Micromegas
- Motivation for a new concept
- The Piggyback Micromegas
- Simulation, experimental set-up and studies with Piggyback detectors
- Towards a sealed chamber
- First result with Caliste readout system
- Conclusions and outlook

- Timepix chip
  - + SiProt 20  $\mu\text{m}$
  - + Micromegas
- Matrix 256 $\times$ 256 pixels of 55 $\times$ 55  $\mu\text{m}^2$
- Chip noise:  $\sim 650 e^-$
- $^{55}\text{Fe}$  source (6 keV)
- Ne/Iso (90:10)
- TOT mode
- Drift distance  $\sim 5$  mm



- Determination of the polarization:
  - ✦ Barycentre & principal axis
  - ✦ Reconstructed absorption point
- $\varphi$  photoemission angle
- Low  $E_{k\text{-edge}}$  of Neon
  - $e_{\text{auger}}$  are isotropically emitted with a small fraction of the photon energy
- In low Z gas mixture tracks are longer so angular reconstruction is easier



Photoelectron +  $e_{\text{auger}}$  track in Neon+10% Isobutane

Attié *et al.*, NIMA 610(2009)178A

1. Sealed detector needed for space experiment
2. In gaseous detectors: sparks induced by heavily ionizing particles:
  - Limitations on rate during operation
  - Reduction of the detector lifetime
  - Risk of damage of the readout electronics



Most of the time no issue (detector/electronics) using passive protection

→ Possible solutions:

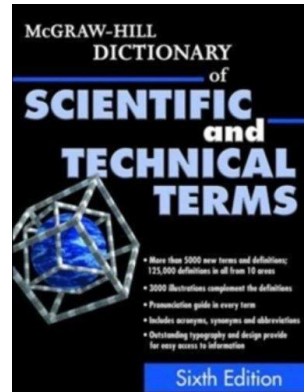
1. Limit usage of outgassing material
2. Spark suppression by using:
  - Resistive strips above readout (ATLAS/NSW Upgrade)
  - Resistive foil on top of anode plane

### Piggyback resistive Micromegas

- Attié *et al.*, JINST 8 P05019 (2013)
- inspired by similar work with PPAC by M. Kocsisa *et al.*, NIMA 563 (2006) 172–176
- new approach where a thin resistive layer is desposited on an adequate insulator

- McGraw-Hill Science & Technology Terms: **piggyback board**

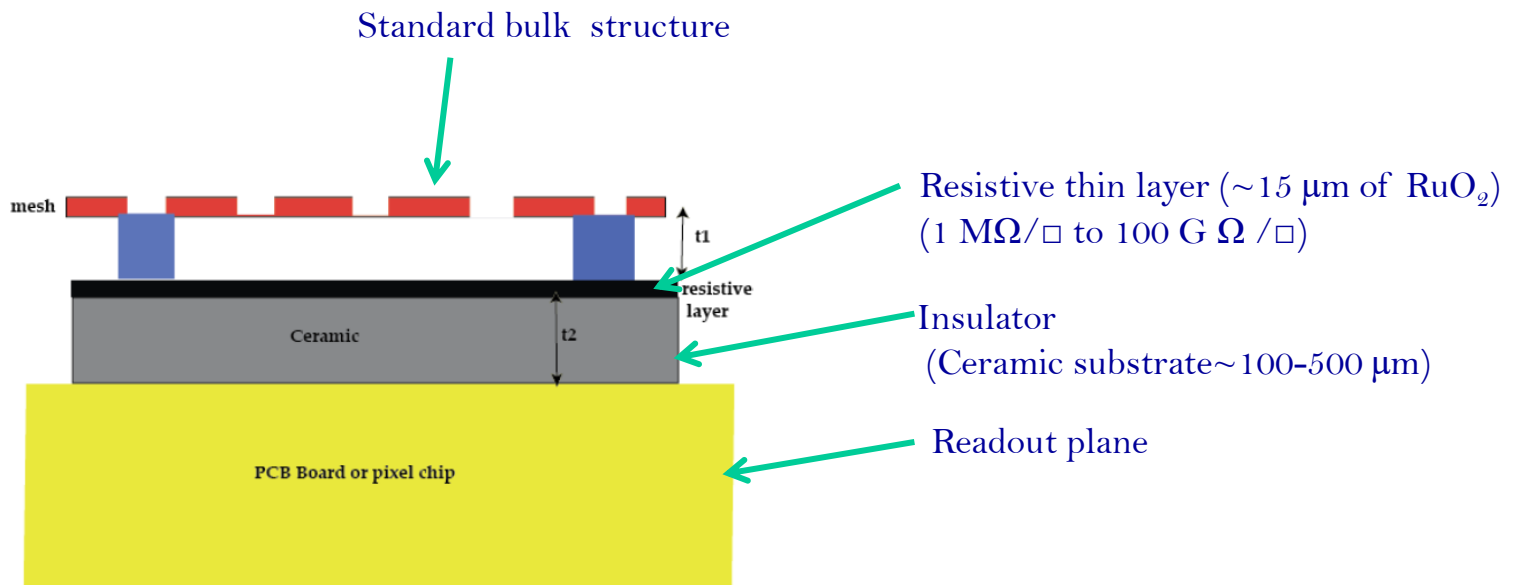
“A small printed circuit board that is mounted on a larger board to provide additional circuitry.”



- Piggyback (transportation): something that is riding on the back of something else



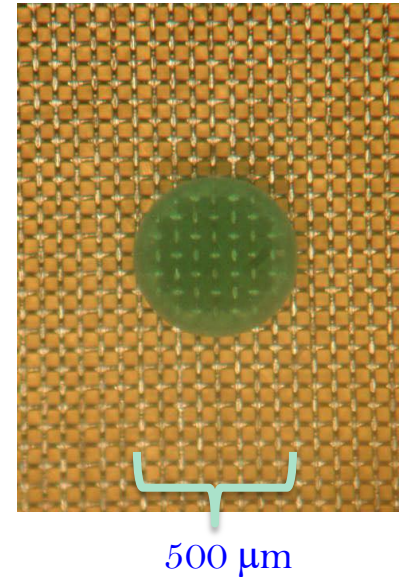
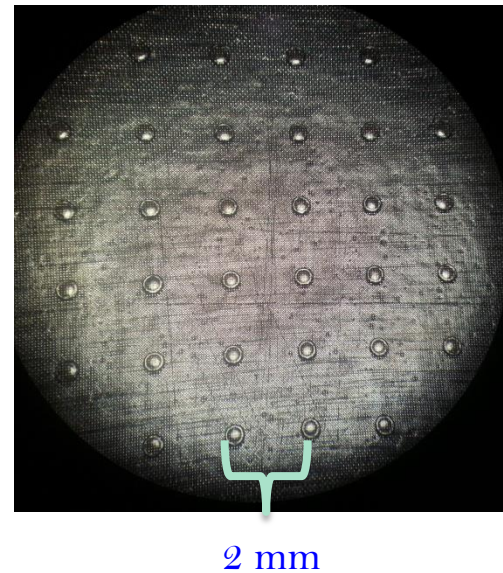
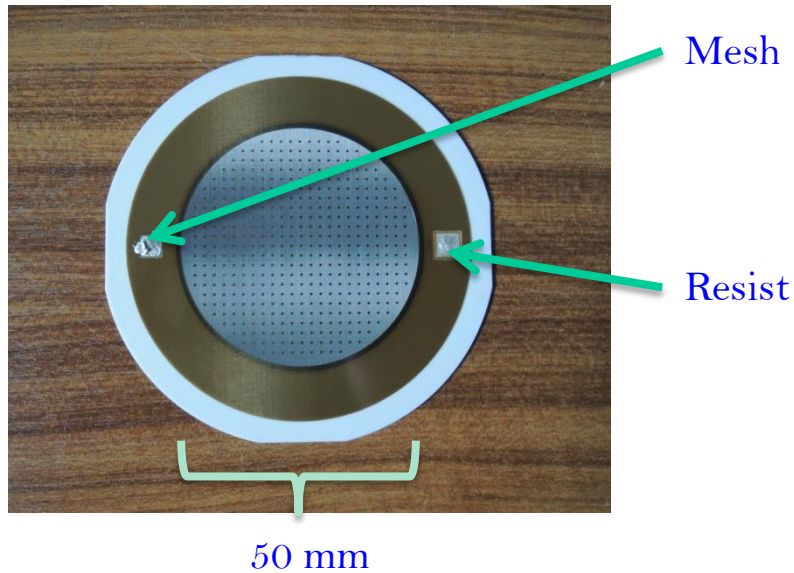
- Separation of the amplification structure and the readout plane



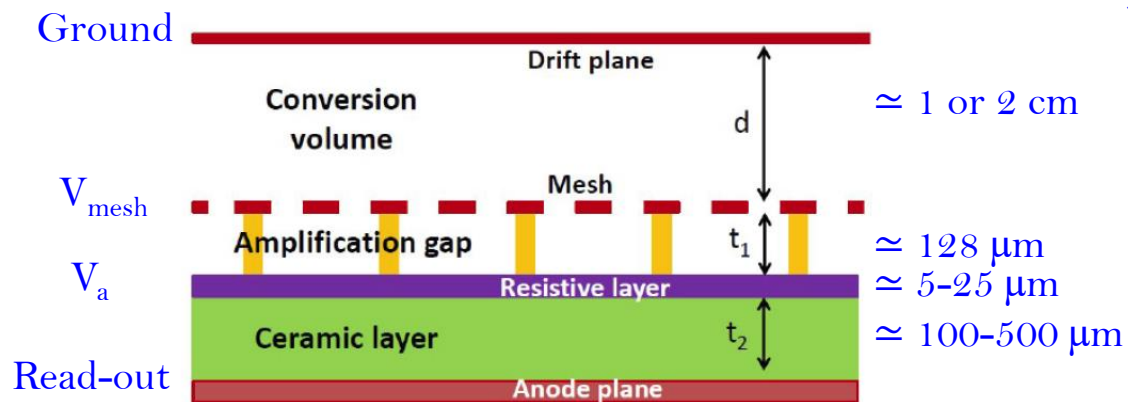
- Signal is transmitted by capacitive coupling to the readout plane
- Optimisation of the induced signals:  $t_{\text{insulator}} \ll t_{\text{gas}} \frac{\epsilon_{\text{insulator}}}{\epsilon_{\text{gas}}}$
- $\epsilon_{\text{insulator}}$  should be as high as possible (first prototype alumina with  $\epsilon \sim 10$ )

- 128  $\mu\text{m}$  amplification gap, 20  $\mu\text{m}$  of  $\text{RuO}_2$  with 100  $\text{M}\Omega/\square$ , ceramic layer 300  $\mu\text{m}$

## Bulk Micromegas

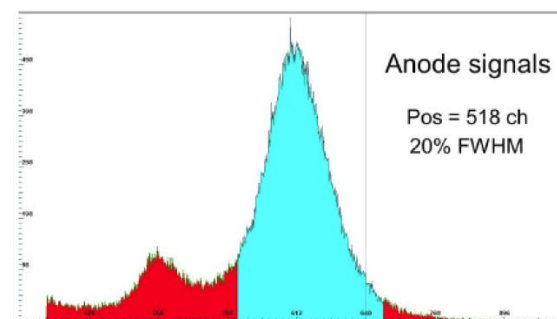
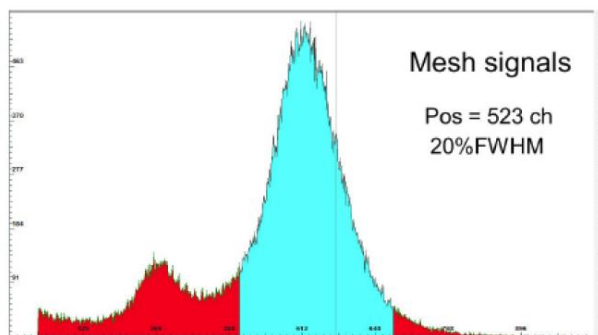


« 45/18 » woven mesh

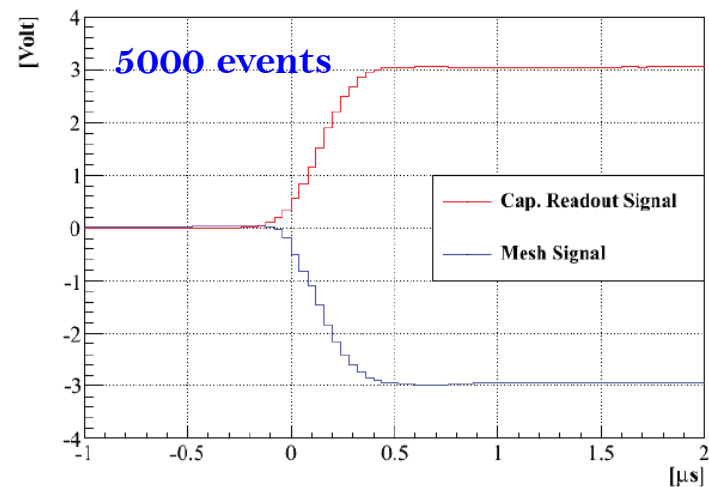
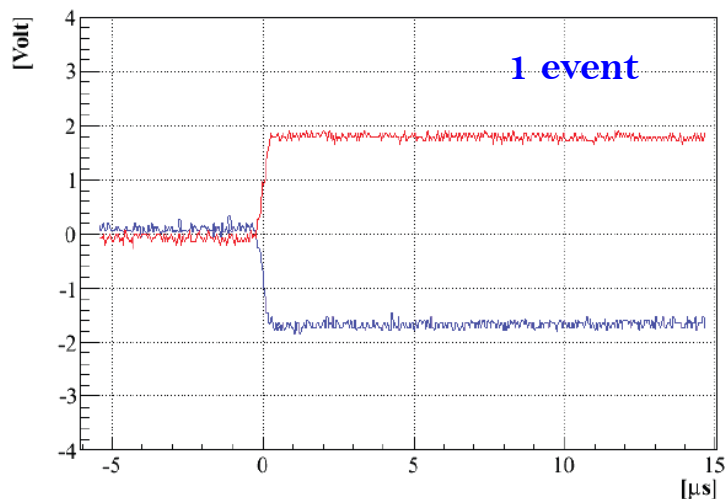




- Checking possible losses by the ceramic layer: signal entirely transmitted



- Test with a  $^{252}\text{Cf}$  (fission fragments signals) reading simultaneously mesh and anode



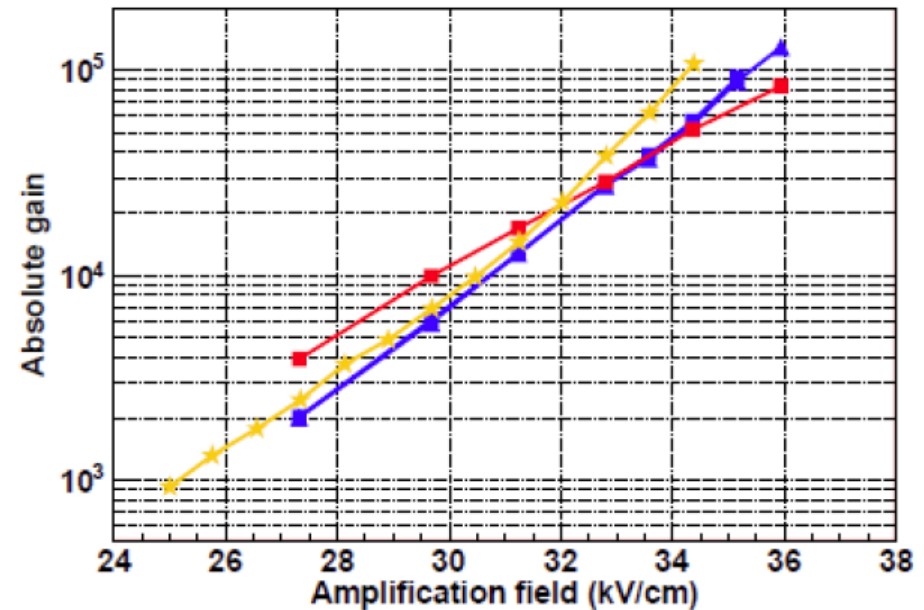
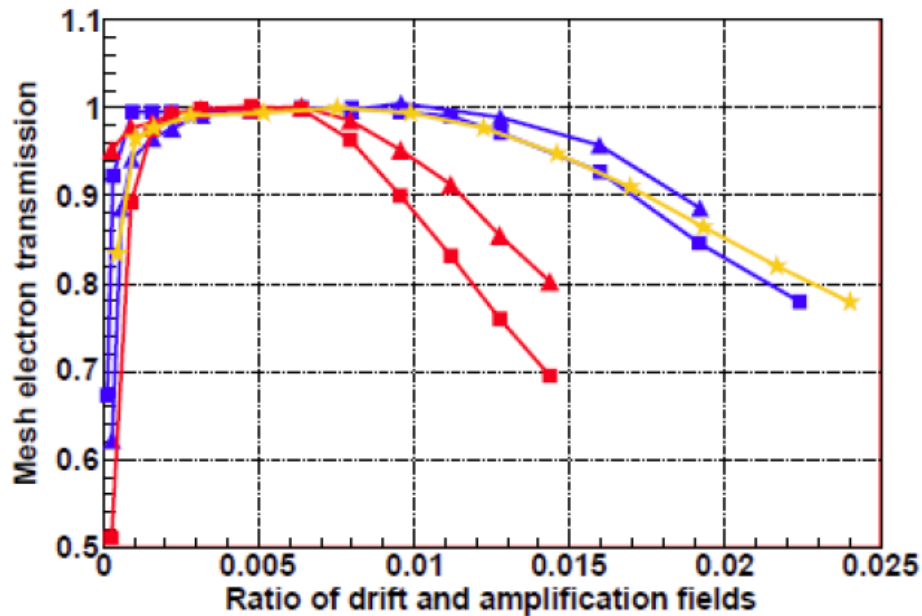
→ Amplitude within 5%, same rise time for both polarities

- Electron transparency reach a plateau
- Gain up to  $10^5$

Ne + 5% Ethane

Argon + 5% Iso

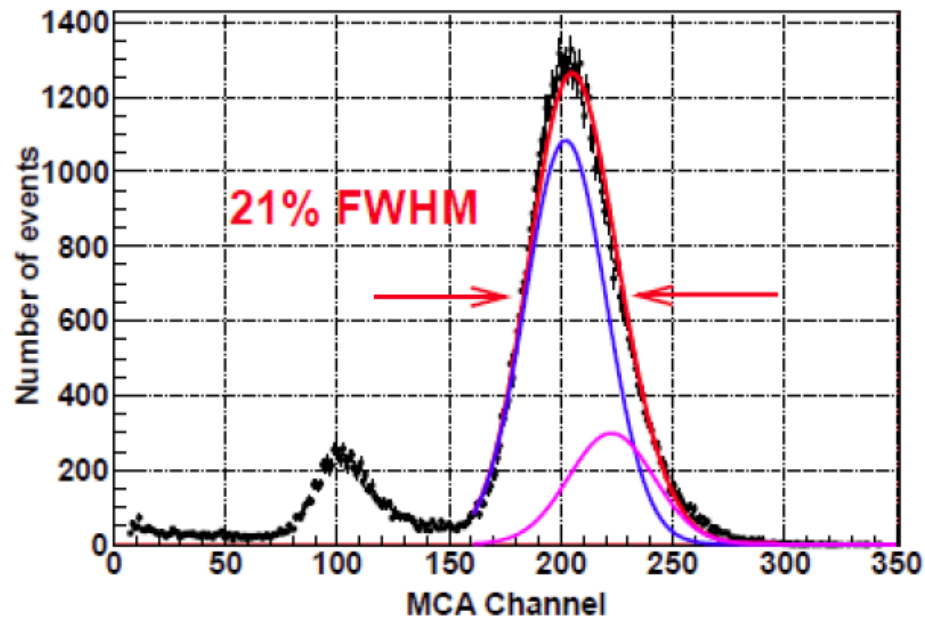
Argon + 5% Iso (Standard Bulk)



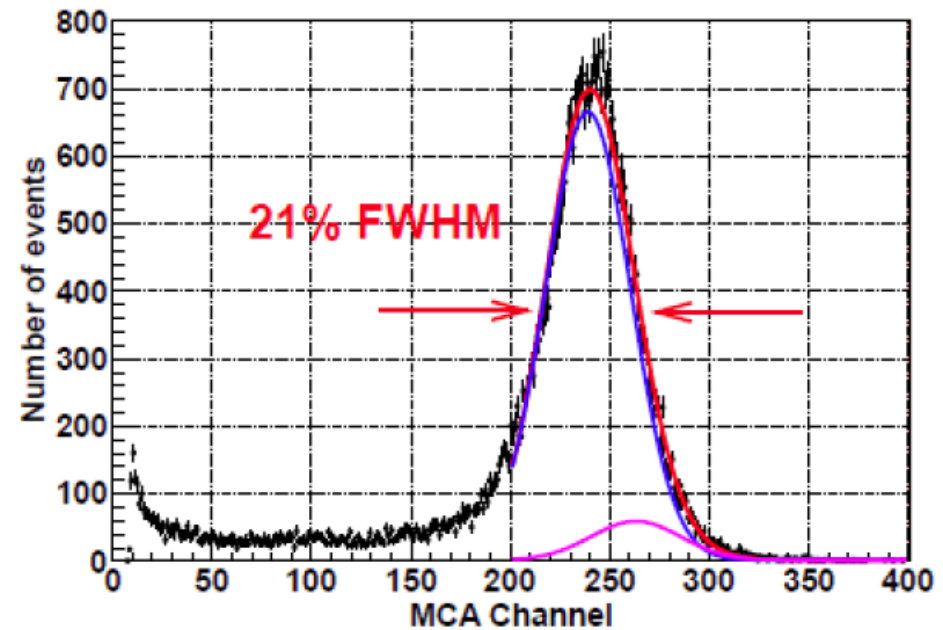
Attie *et al.*, JINST 8 P05019 (2013)

- Energy resolution in two gas mixtures: 21% FWHM

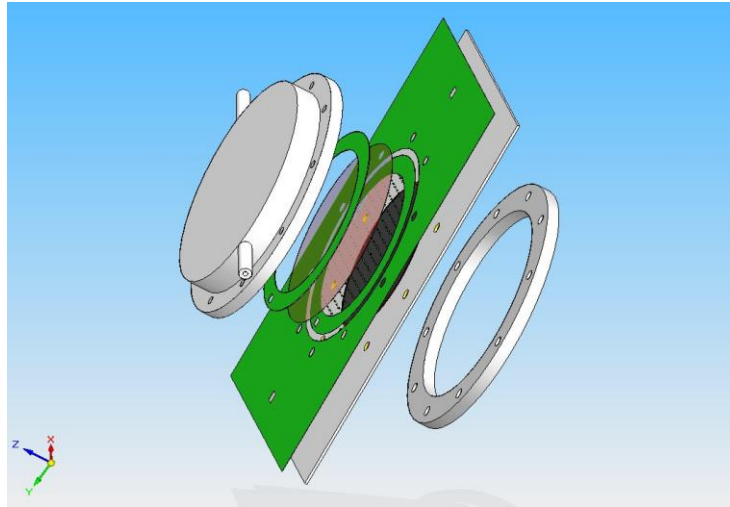
Argon + 5% Iso



Ne + 5% Ethane

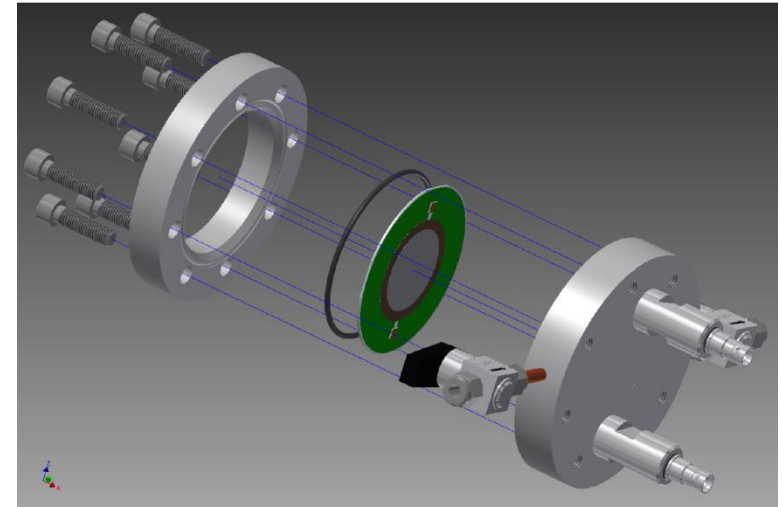


Attie *et al.*, JINST 8 P05019 (2013)



- HV connectors outside
- Ceramic partially outside
- Made in aluminium
- PCB Board under ceramic layer

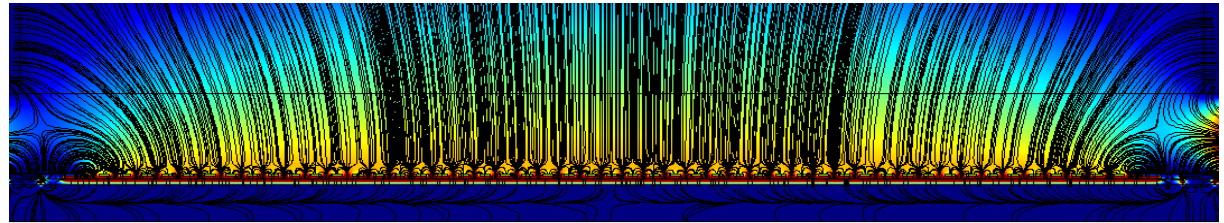
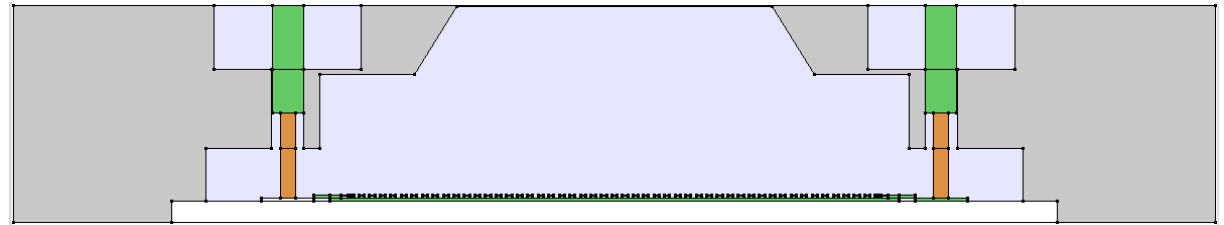
→ Verify the resistive layer concept  
Good performances in normal mode



- HV connectors inside
- Ceramic totally inside
- Made in stainless steel
- Uncovered ceramic layer

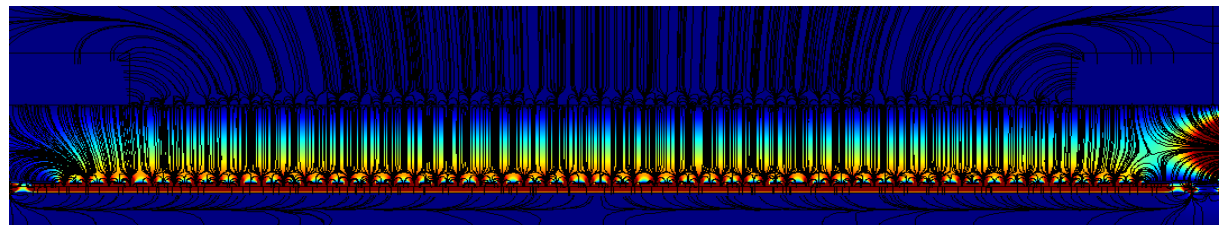
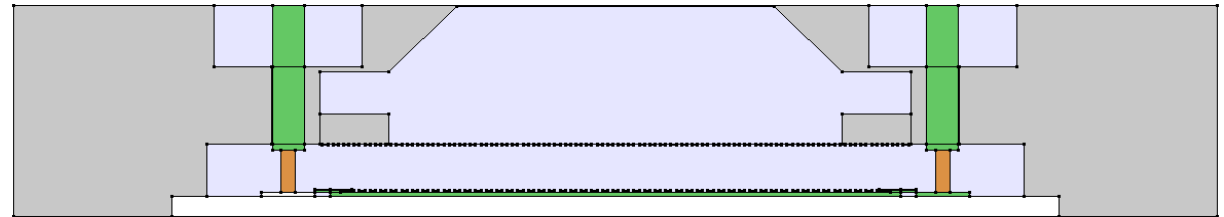
→ Very low outgassing  
Robust and versatile

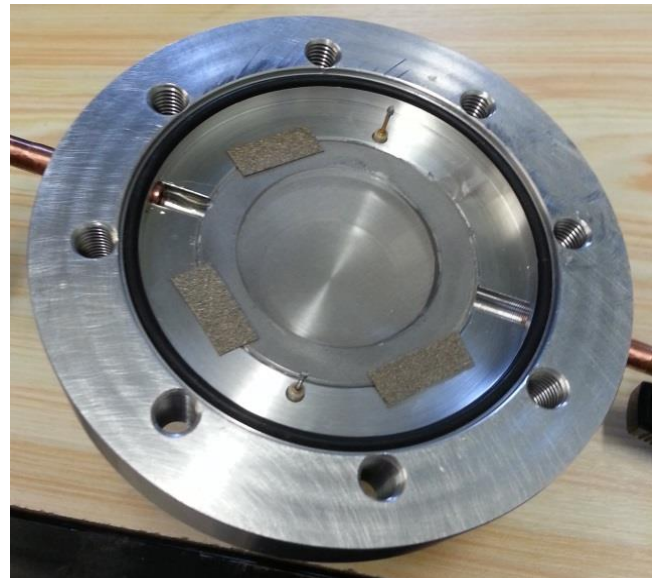
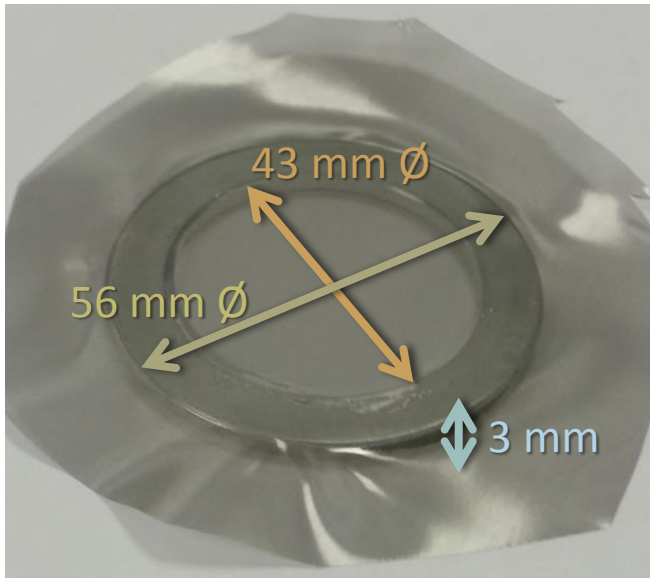
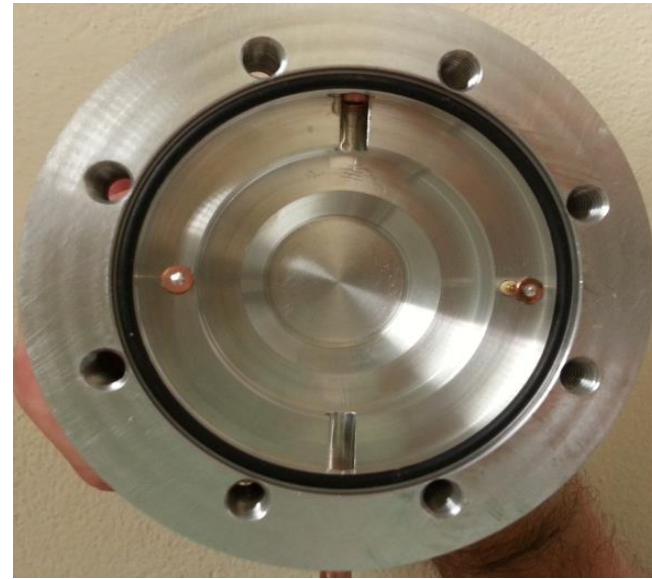
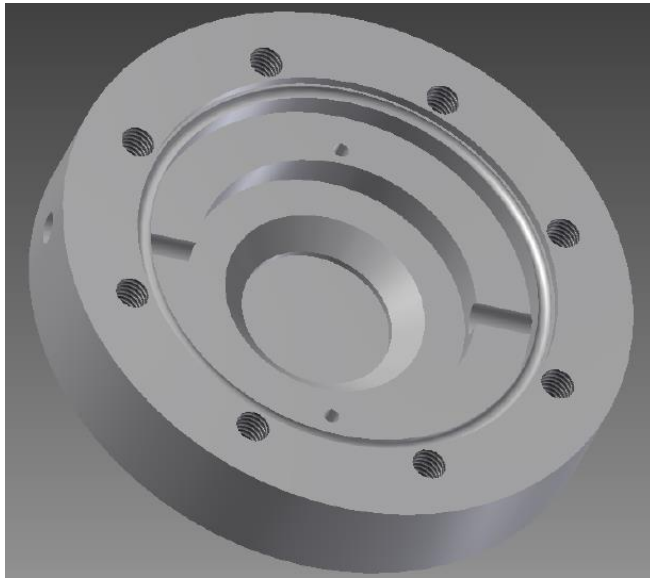
- Problem:  
Bad parallelism of  
electric field lines



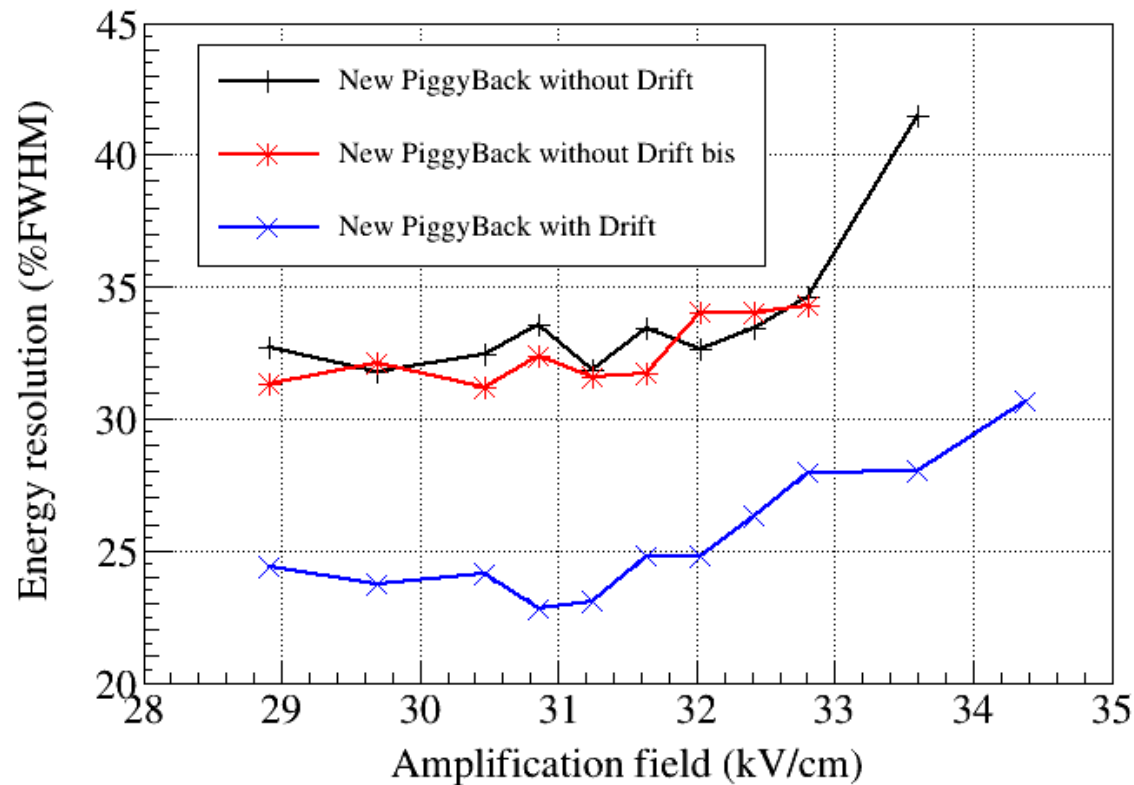
→ Idea: Put a grounded element into the chamber to improve the parallelism

- Solution:  
Install an additional  
little mesh to make  
the drift space more  
homogenous





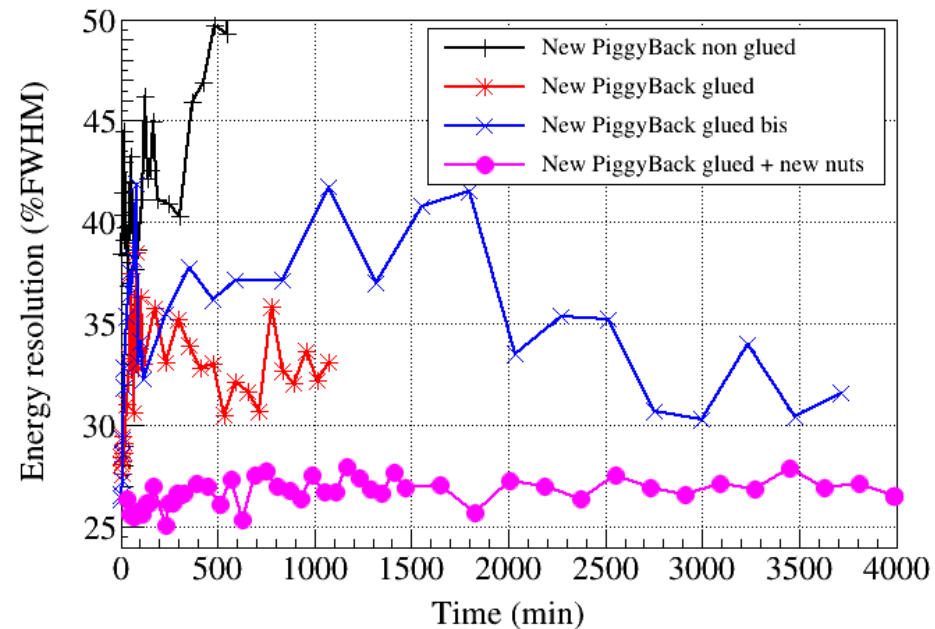
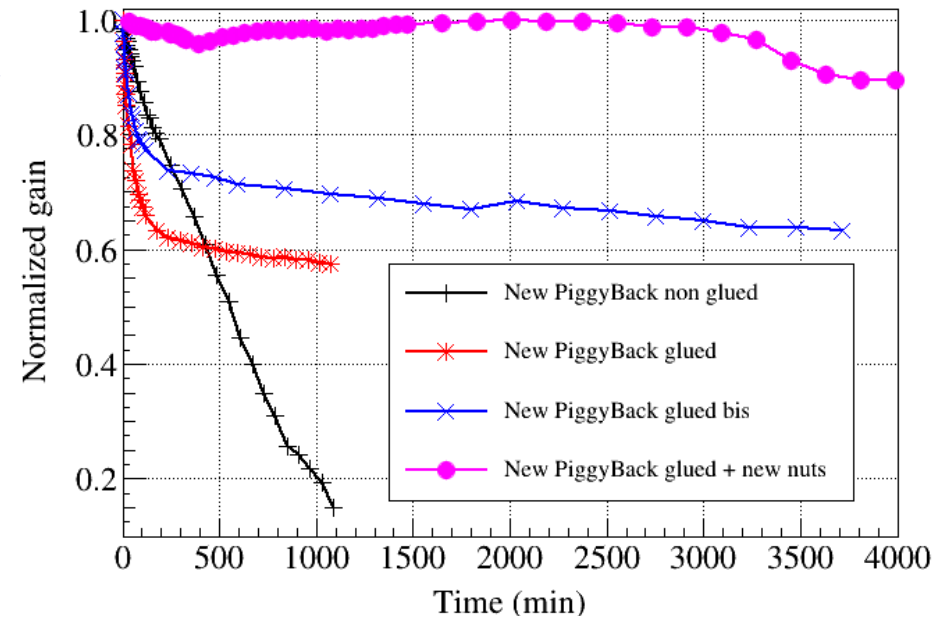
- Electron transmission maximal for:  $0.001 < \frac{E_{\text{Drift}}}{E_{\text{Amplification}}} < 0.015$
- Gain up to  $5 \times 10^4$
- Energy resolution  $\leq 25\%$  for a large range of amplification fields



The new chamber meets the expected performance in normal operation

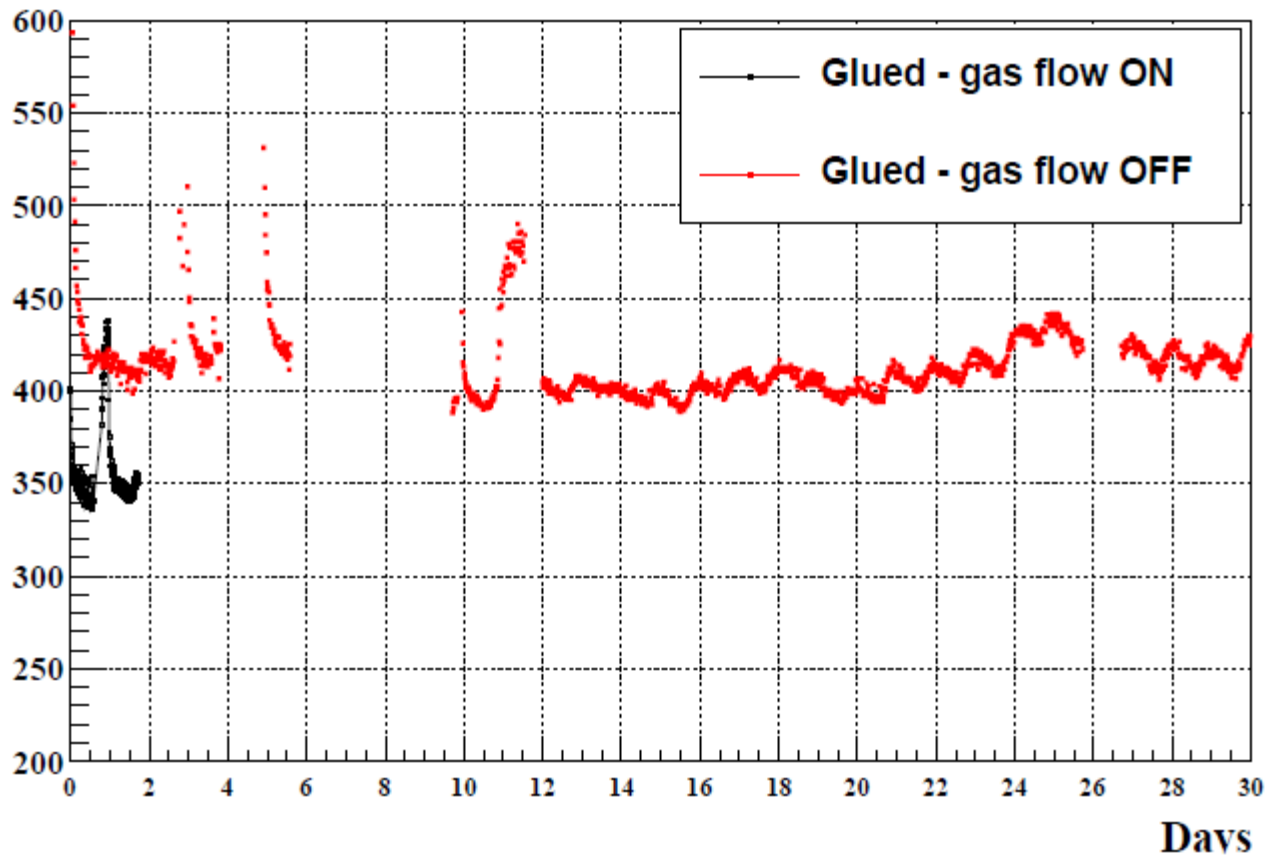
- Evolution of gain and energy resolution during several days:  
→ Important gas leaks
- Solutions:
  - Torr Seal glue on HV connectors
  - Improvement of the mechanics

The new chamber is now leak-proof

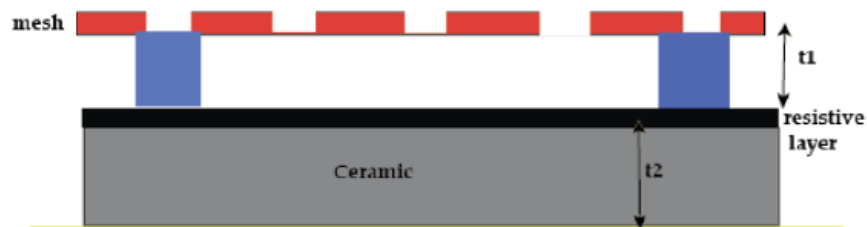




- Stability over 30 days
- Small fluctuations due to day night effects
- Big amplitude fluctuations due to the removal of the source



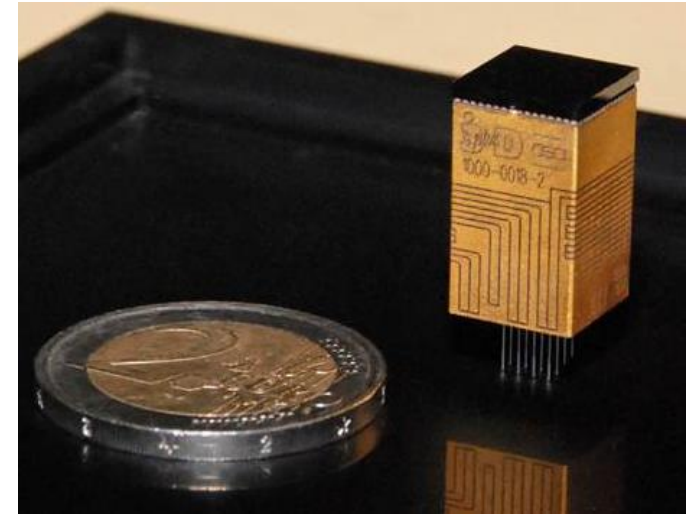
- Why?
  - Low noise, good energy resolution, radiation hardness, large size, low cost,...
  - Could work at any temperature
  - Improved performance for space missions
- How?
  - Put the electronics at the bottom of the ceramic layer
  - Signal transmission by capacitive transmission



? ¿ High-tech electronics ? ¿



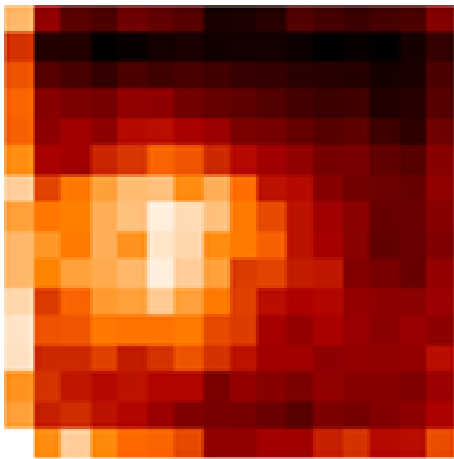
- Space qualified device and optimised for space missions
- Very compact and robuste
- Matrix of 256 pixels of  $625 \mu\text{m}^2$
- No dead-space
- Made of 8 eight programmable ASICs
- Noise  $e^-$ :  $25.5e^- + 5.5e^-/\text{pF}$
- Spectral resolution: 1.2 keV FWHM@ 60 keV



Features	Caliste HD
<i>Dimensions of the 3D block</i>	10 x 10 x 20.7 mm <sup>3</sup>
<i>Electrical IIF Pin grid array</i>	4 x 4 (1.27 mm pitch)
<i>Number of pixels</i>	256 (16 x 16)
<i>Pixel pitch</i>	625 $\mu\text{m}$
<i>Guard ring width</i>	20 $\mu\text{m}$
<i>Number of ASIC</i>	8
<i>ASIC version</i>	IDeF-X HD (32 channels)
<i>Slow control</i>	Yes
	3.3V

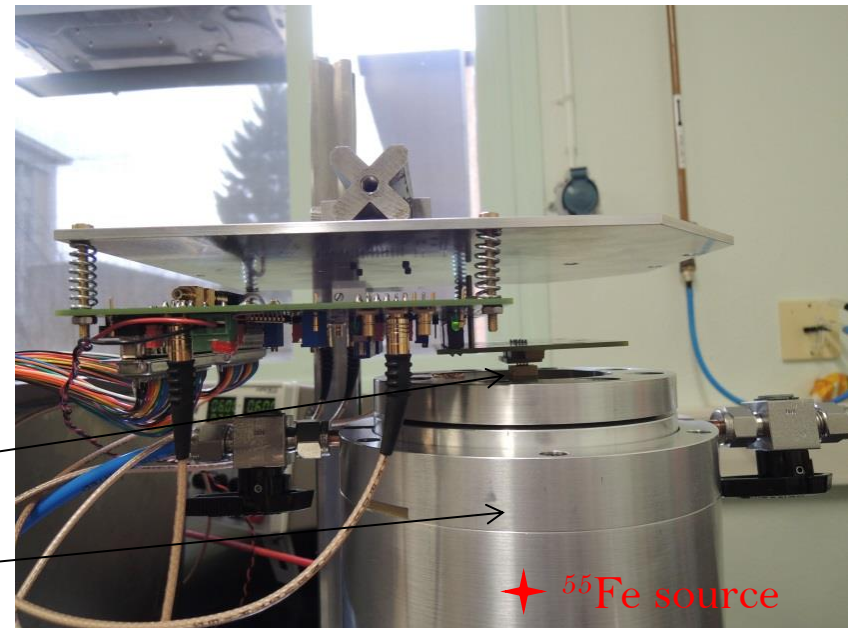
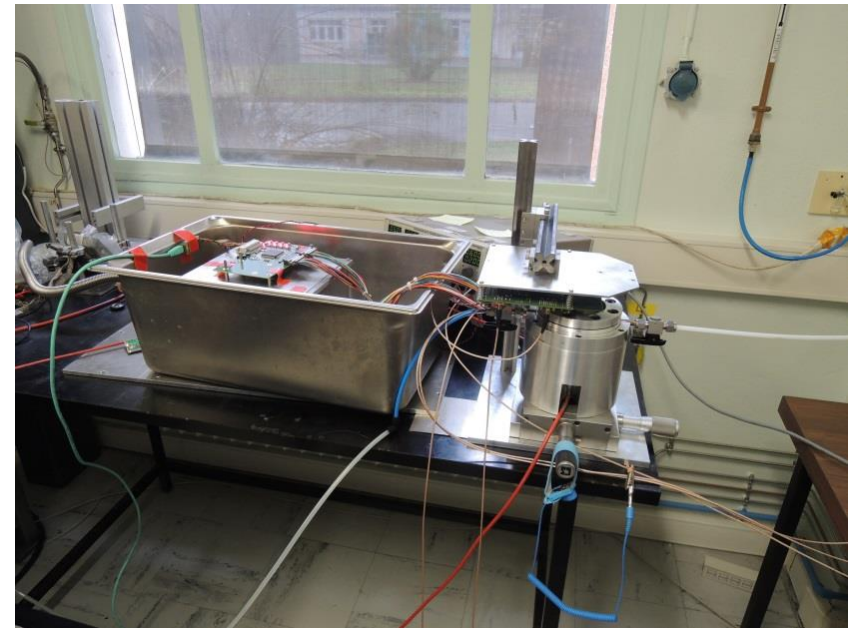
A. Meuris *et al.*, NSSMIC(2011)4485-4488

- Distance between Piggyback and Caliste readout: 0.5 to 1 mm!!!
- Micrometric moving support
- Data taken with  $^{55}\text{Fe}$

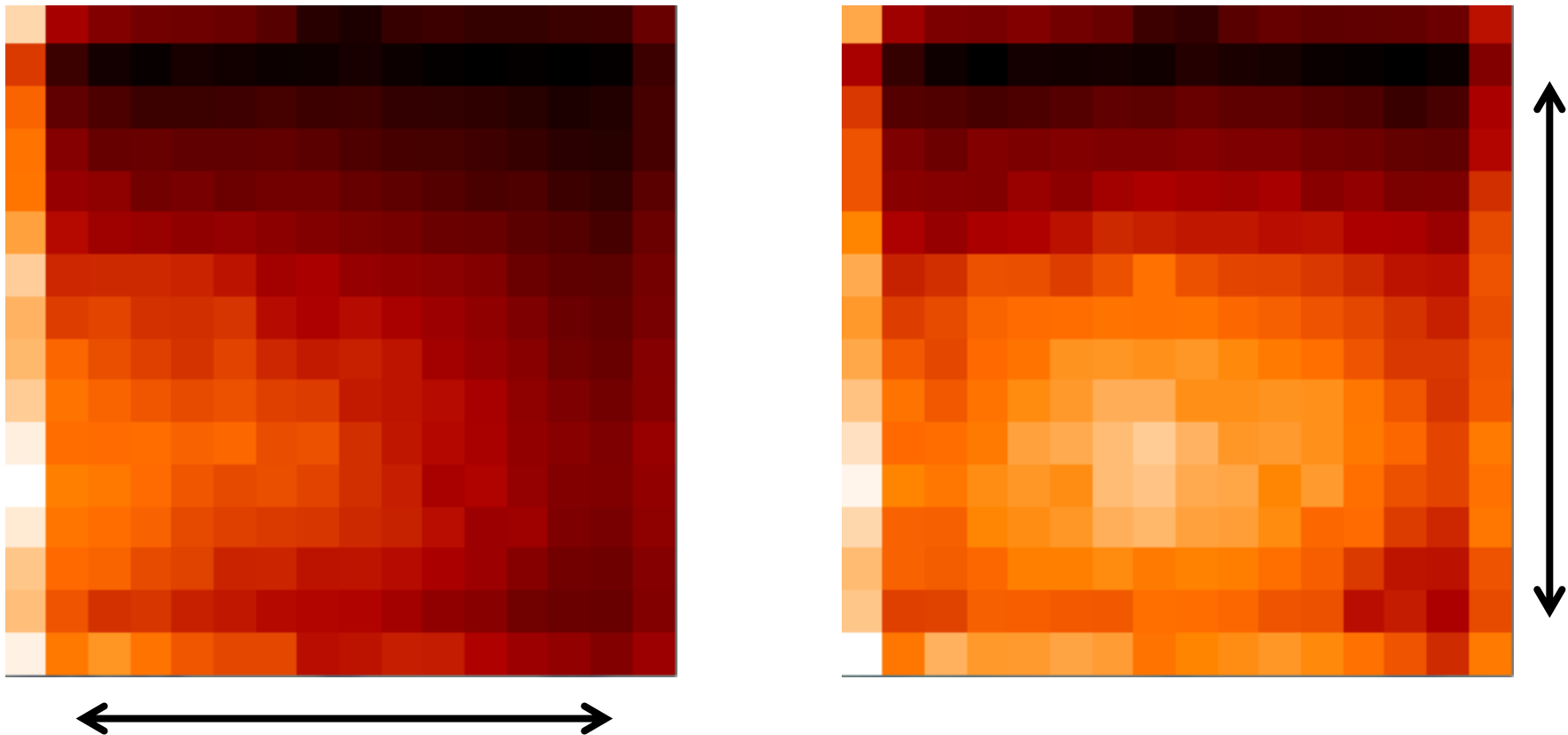


Caliste readout

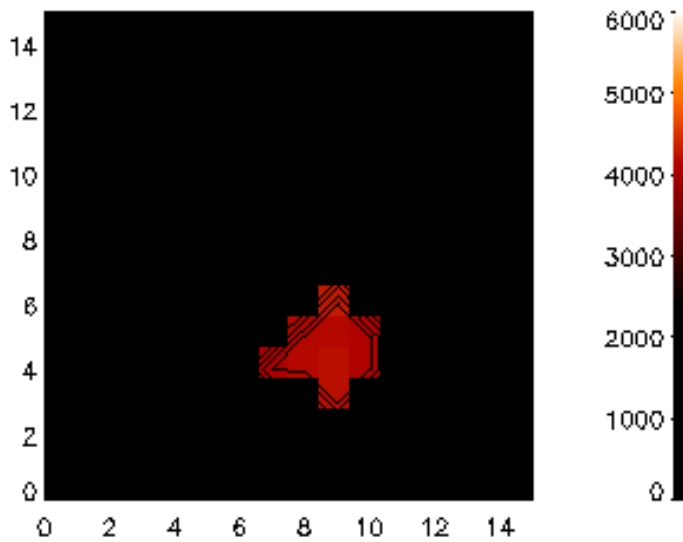
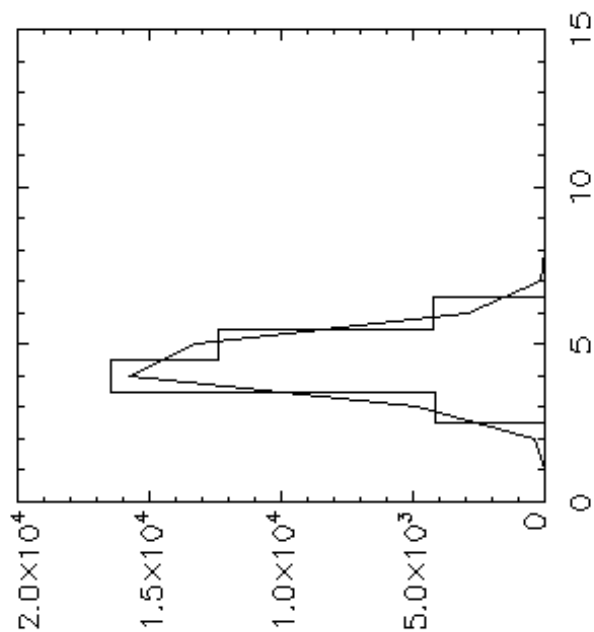
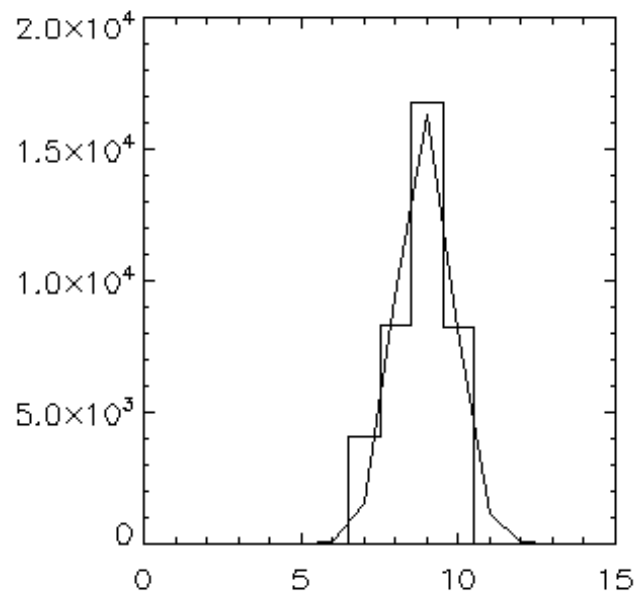
Piggyback detector



- Integral of many events while moving the source

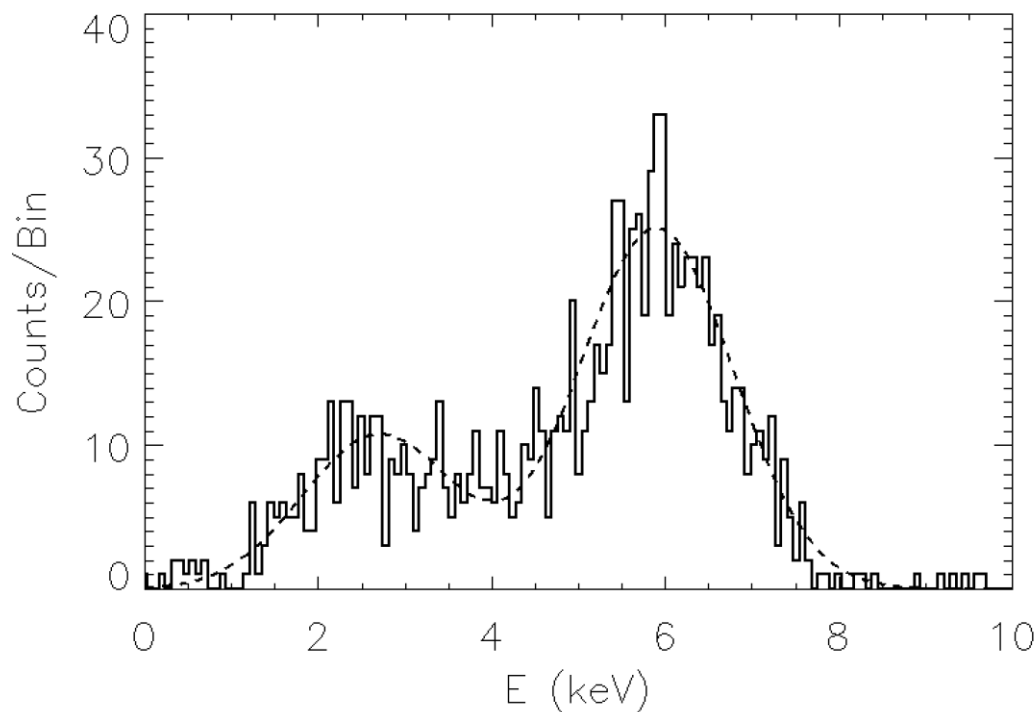


First time that MicroMEGAS are successfully coupled to space electronics!



- **No calibration** (offset or gain correction per pixel): FWHM=35% @ 5.9 keV
- Selection:
  - event where spots are complete
  - spots diameter > 2.3 pixels rms
- histogram of total charge (29 pixels)
- Fit by two gaussian with identical width at 2.7 (5.9-3.2) keV and 5.9 keV

Very Preliminary!

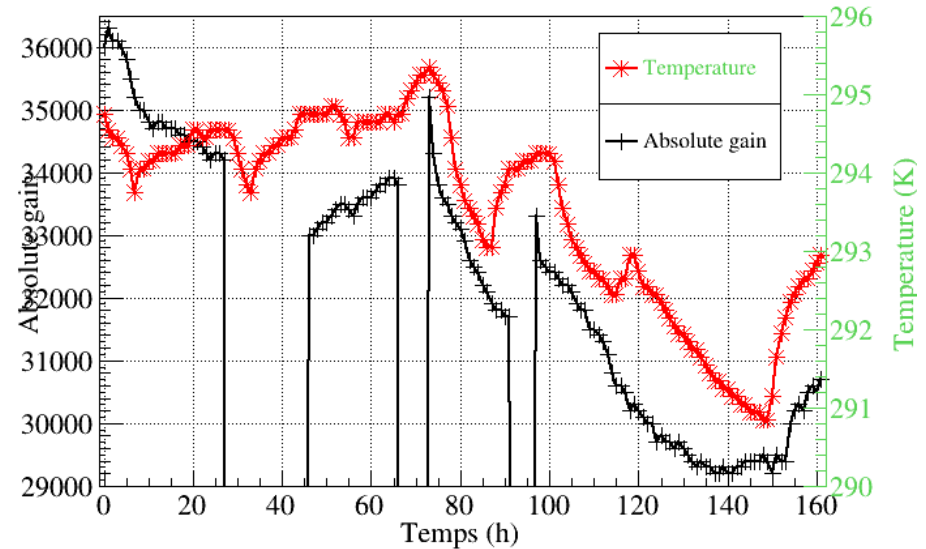
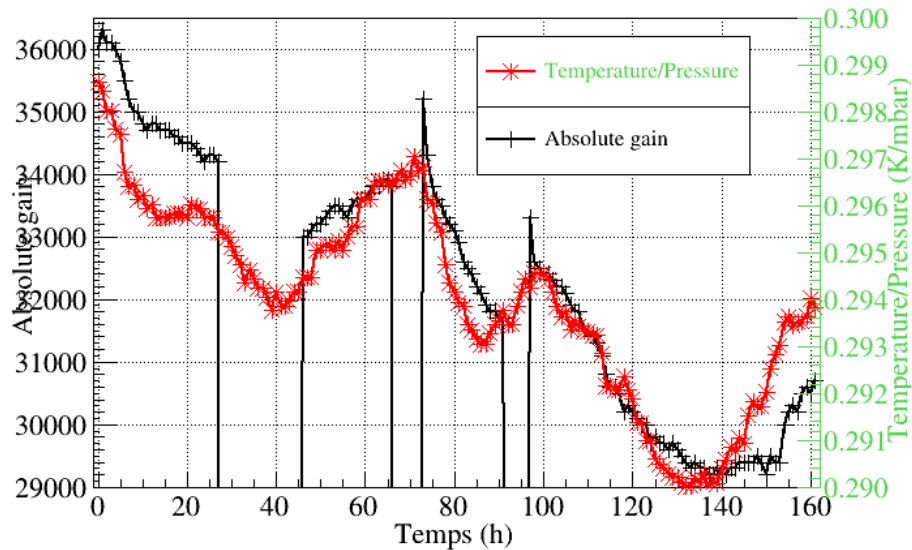


- New resistive Micromegas concept (Piggyback) brings:
  1. - Material compatible with space operation
    - Extensible to larger volume/area gas detectors for polarimetry at higher energy (1 MeV-1 GeV range)
  2. - Electronics coupling using capacitive transmission fully spark-proof
    - Compatible with very low noise ( $30 e^-$ ) electronics readout (Caliste readout)
- First try, but further studies will be done
- Toward a new gas-based imaging spectrometer/polarimeter in the soft X-ray domain!



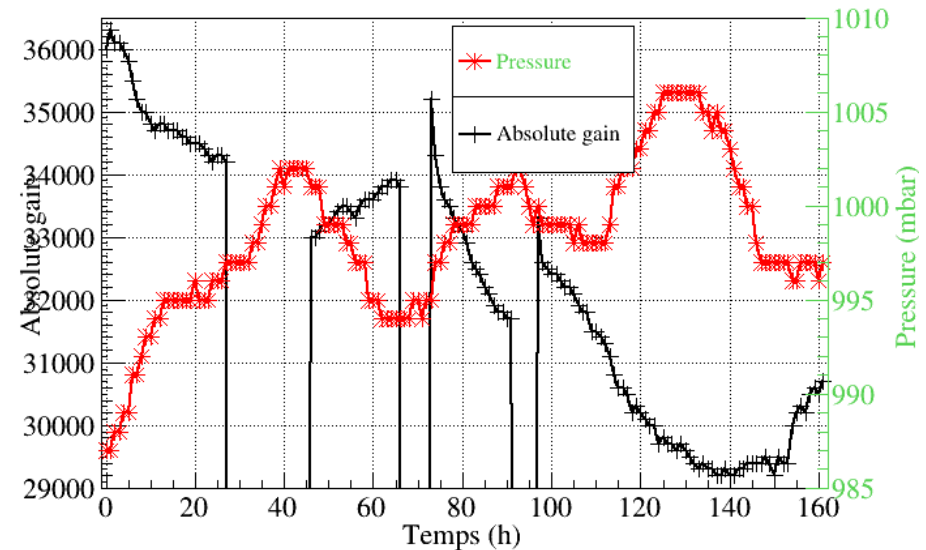






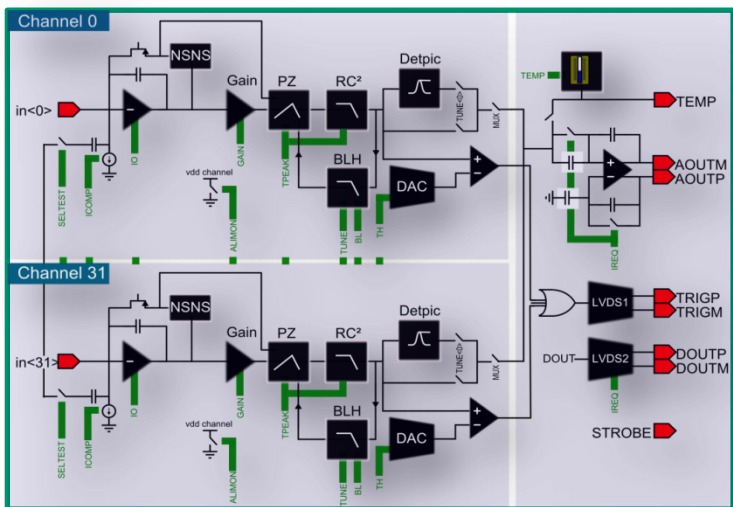
Mixing ratio	$C_P$ (1/mbar)	$C_T$ (1/K)
80/20	-0.46	1.50
90/10	-0.59	1.91
95/5	-0.68	2.18

*Adloff et al., Environmental study of a Micromegas detector*



Evolutions induced by the environment have to be taken into account

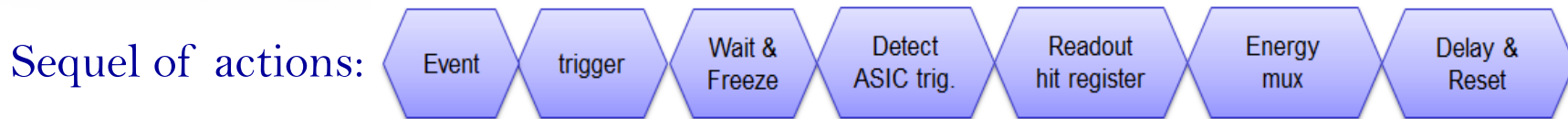
## Architecture of one IDeF-X HD ASIC:



For each channel: low noise/low charge preamplifier + sharper with adjustable peaking time + discriminator to set the low threshold value + peak detector to memorize the pulse height

Signal induced in pixels → trigger set up and sent to SAB.

FPGA begins readout sequence channel by channel : date, adress, energy



+ FPGA card

+ SAB card

- In permanent reconfiguration with logical gates
- Users communicate with the card by scripts
- Communication between the electronics and the computer